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APPLICANTS Hiroaki Kikuchi, Chitose, JAPAN; Toshiaki Sawada, Koganei, JAPAN; Hirohiko Yamamoto, Hachioji, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/00068 01/08/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-2507 01/09/2002				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 13
				INDEPENDENT CLAIMS 8
ADDRESS 020457				
TITLE FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE				
FILING FEE RECEIVED 1350	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	

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